

FIG. 1

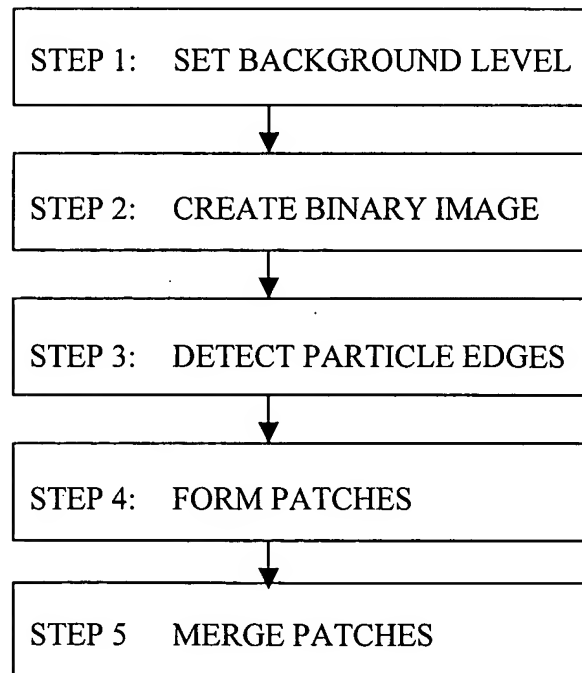


FIG. 2

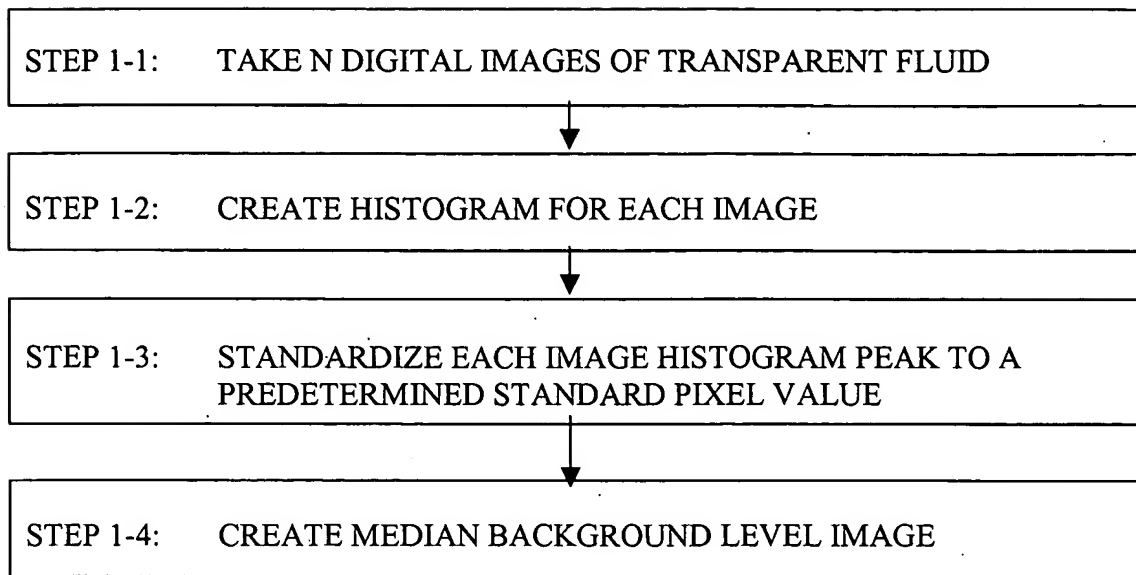


FIG. 3

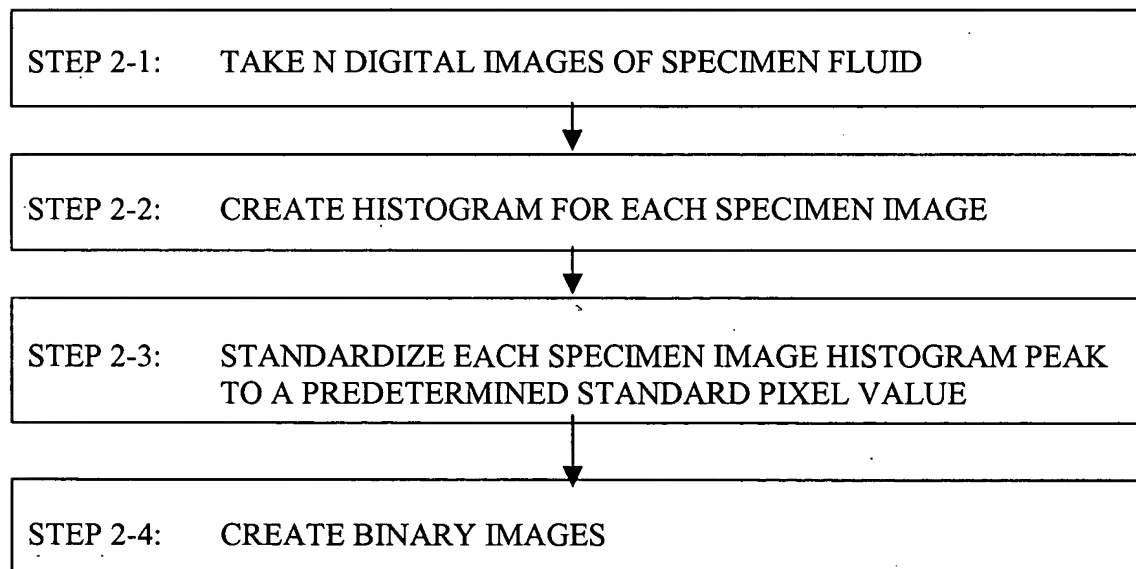


FIG. 4



Diagram illustrating a 20x20 grid of bits (0s and 1s) with three overlapping rectangles labeled P1, P2, and P3. The grid contains various patterns of 1s and 0s, with some 1s forming a diagonal-like structure.

FIG. 7

The diagram illustrates a 20x20 grid of bits. Three specific regions are highlighted with rectangles and labeled:

- P4**: A rectangle in the top row, spanning columns 4 to 8.
- P5**: A rectangle in the middle, spanning columns 4 to 12.
- P3**: A rectangle in the bottom, spanning columns 4 to 12.

The grid contains a mix of 0s and 1s, with some patterns appearing within the highlighted regions.

FIG. 8A

P4

P6

FIG. 8B

[illegible]

FIG. 8C